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ATTORNEY'S DOCKET NO: A95003C3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: McKain *et al.*
Serial No: 08/932,993
Filed: September 18, 1997
For: Portable Moving Picture Recording Device Including
Switching Control For Multiple Data Flow Configurations
Examiner: L.T. Nguyen
Art Unit: 2612

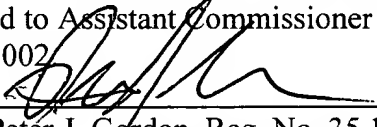
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Technology Center 2600

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on December 16, 2002.


Peter J. Gordon, Reg. No. 35,164

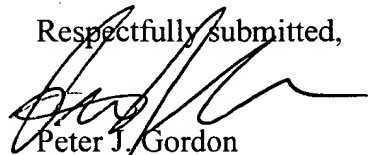
Assistant Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER

Transmitted herewith for filing in the above patent application is a Reply to the Office Action mailed August 14, 2002, Marked Up Version Of The Specification and Marked Up Version of Figure 4 with change shown in red. A one-month extension of time in which to respond is hereby requested.

Please charge **Deposit Account No. 50-0876** in the amount of **\$110.00** extension fee. The Commissioner is hereby authorized to charge an additional fees which may be required or credit any overpayment to said Deposit Account. A duplicate copy of the sheet is enclosed.

Respectfully submitted,



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Dated: December 16, 2002

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#20/Amend E

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Peter J. Gordon, Reg. No. 35,164

Assistant Commissioner for Patents
Washington, D.C. 20231

REPLY

Sir:

In response to the Office Action mailed August 14, 2002, the Applicant submits this reply. A one-month extension of time to respond is hereby requested.

Please amend this patent application as follows.

IN THE SPECIFICATION

On page 7:

Please rewrite the paragraph starting at line 20 and ending on page 8 at line 3 with the following:

Having now described a general mechanical structure of an embodiment of the moving picture recorder, the electronic circuitry for processing the moving pictures will now be described. The output of the video camera 22, which is generally an analog signal, is provided to the digitizing electronic circuitry such as shown in Figs. 3-9. Referring to Fig. 3, such circuitry